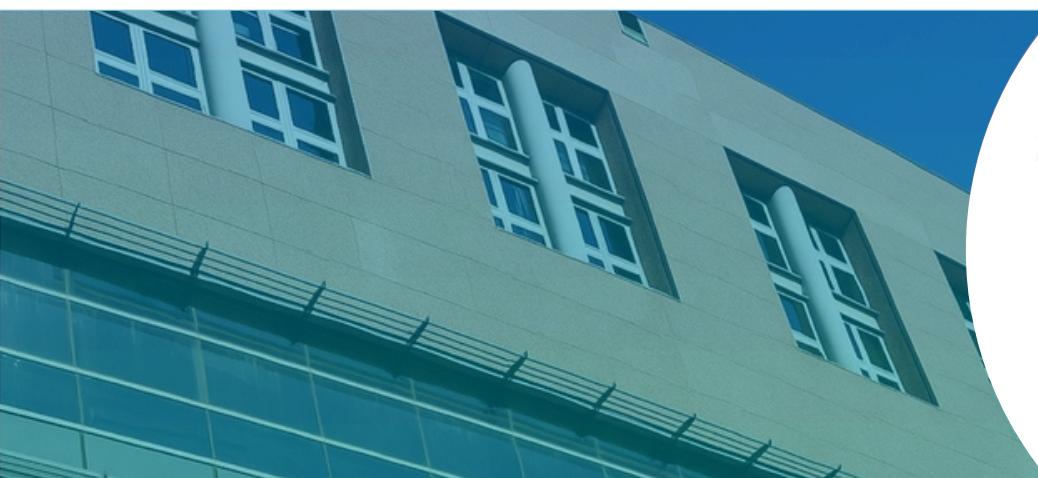# Talk@DAUIN

Deep test using reverse engineering to address security issues in processors

Open to: everyone

29 January, 2026 - 10:00 am
Meeting room Luigi Ciminiera (5th floor)

Ying
ZHANG
Tonji University (China)

The Chinese government is deeply concerned about the security risks associated with chips and has funded multiple related projects. Hence we are researching the deep test using reverse engineering to address security issues in processors.
Through this project, we aim to prevent processors with security vulnerabilities from entering critical institutions such as government and military organizations.

Associate Professor, obtained his PhD from the Institute of Computing Technology, Chinese Academy of Sciences in 2011. He has consistently pursued research in integrated circuit testing, fault tolerance, and security, serving as principal investigator on multiple projects funded by the National Natural Science Foundation of China. He has published over seventy academic papers, including more than ten as first author in IEEE Trans. on CAD, ACM Trans. on DAES, IEEE Trans. on VLSI, and so on. He holds ten nationally authorised invention patents.