



Politecnico
di Torino

Dipartimento
di Automatica e Informatica



Talk@DAUIN

Supply Chain Vulnerabilities for ICs and Mitigation Through Design- for-Trust



Open to: **everyone**



25 February, 2026 - 2:00 pm

Meeting room “Luigi Ciminiera”
(5th floor) - DAUIN



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The globalization of Integrated Circuit (IC) design and manufacturing is making designers and users of ICs reassess their trust in hardware. As the IC design flow spans the globe-driven by cost-conscious consumer electronics, hardware is increasingly prone to reverse engineering, Intellectual Property (IP) piracy, and malicious modifications (i.e., hardware trojans).

This talk will explain various forms of threats that the electronic chip supply chain is up against, as well as one particular defense against these threats - logic locking - by covering its basics and evolution.

Professor of electrical and computer engineering at the New York University Abu Dhabi where he is also the Associate Dean of Engineering. He earned his B.S. degrees, one in Electrical and Electronics Engineering and one in Computer Engineering, both from Bogazici University, Turkey in 1999. He obtained his MS and PhD in Computer Science and Engineering from University of California San Diego in 2001 and 2004, respectively. He has industry experience at Texas Instruments, IBM and Qualcomm, and has been with NYU Abu Dhabi since 2010. Prof. Sinanoglu's research interests include design-for-test, design-for-security and design-for-trust for VLSI circuits, where he has around 300 conference and journal papers, and 20 issued and pending US Patents.